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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): S. YAMADA et al
Serial No.: 09/767,830
Filed: January 24, 2001
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND PROCESS FOR MANUFACTURING THE SAME
Group: 2812
Examiner: J. KENNEDY
CONF. No. 2196
Allowed: March 15, 2004 Issue Fee Not Yet Paid

AMENDMENT PURSUANT TO 37 CFR §1.312

MS: ISSUE FEE

Commissioner For Patents
POB 1450
Alexandria, VA 22313-1450

June 9, 2004

Sir:

In accordance with 37 CFR §1.312, it is respectfully requested that the following amendments and remarks be entered, in connection with the above-identified application, as listed below and as set forth on the following pages:

Amendments to the Substitute Specification:

Remarks are included following the Amendments.